

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al. Art Unit : 2673
Serial No. : 09/504,235 Examiner : David L. Lewis
Filed : February 15, 2000 Confirmation No.: 7610
Notice of Allowance Date: November 12, 2004
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed November 12, 2004, enclosed are a completed issue fee transmittal form PTOL-85b and a check for \$1430 for the required fee, including patent copies.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: February 10, 2005



John F. Hayden
Reg. No. 37,640

Customer No. 26171
Fish & Richardson P.C.
1425 K Street, N.W., 11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331